APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/661,223	09/13/2000	Futoshi Kaibuki	450100-02710	7775

20999 7590 09/22/2005		EXAMINER
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ART UNIT	PAPER NUMBER
2112	

DATE MAILED: 09/22/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
Office Action Summary	09/661,223	KAIBUKI, FUTOSHI
	Examiner	Art Unit
	Kim T. Huynh	2112

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on <u>01 July 2005</u>.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) <u>1,3,9-13,17-19,21,26,28 and 32</u> is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) <u>1, 3, 9-13, 17-19, 21, 26, 28,32</u> is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on <u>14 September 2000</u> is/are: a)☒ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☒ All b)☐ Some * c)☐ None of:

      1.☒ Certified copies of the priority documents have been received.

      2.☐ Certified copies of the priority documents have been received in Application No. _____.

      3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1)☒ Notice of References Cited (PTO-892)

2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3)☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>7/18/05</u>.

4)☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.

5)☐ Notice of Informal Patent Application (PTO-152)

6)☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 103

1.   The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set
> forth in section 102 of this title, if the differences between the subject matter sought to be patented and
> the prior art are such that the subject matter as a whole would have been obvious at the time the
> invention was made to a person having ordinary skill in the art to which said subject matter pertains.
> Patentability shall not be negatived by the manner in which the invention was made.

2.   Claims 1, 3, 9-11, 13, 17-19, 21, 26, 28,32 are rejected under 35 U.S.C. 103(a)

as being unpatentable over Ludtke et al. (Pub. No US 20030210252) in view of

Washino (US Patent 6,370,198) and further in view of Lym et al. (Pub No

US20050097245)

As per claims 1, 19, Ludtke discloses an electronic apparatus for processing

audio/video data, comprising:

- A data processing subunit(fig.3, 60 ie TV), included within said electronic

  apparatus, for receiving and processing audio/video input data; [0039],

  [0015]

- A first functional block (fig.3, 63 ie controller), included within said data

  processing subunit, operative as an audio/visual processing functional

  block to process the audio/visual input data; [0039-0043]

- A second functional block (fig.3, 62, ie display), included within said data

  processing subunit, operative as terminating functional block to terminate

  the data processed by said first functional block by transforming the

  audio/visual data into a user-visible signal and outputting said user-visible

loads packet within the buffer 78 into the VRAM 64 which controlling by controller (first functional block) to be shown on the display 62 (second functional block)

- A memory (fig.3, 78 ie buffer) [0043] for storing information pertaining to said data processing subunit and said second functional block, wherein the information stored in said memory is accessible by an external electronic (fig.3, 100 ie VCR) apparatus connected to said electronic apparatus via a serial data bus (fig.3, 90 ie 1394); and [0014],[0018],[0039]

- Connection means for connecting said electronic apparatus and said external electronic (fig.3, 100 ie VCR) apparatus via said serial data bus(fig.3, 90 ie 1394). [0014]

Ludtke discloses all the limitations as above except by transforming the data to an image signal. However, Washino discloses interface unit operative to convert the video program in the input format into an output signal representative of formatted image, and output the signal to an attached display device. (col.4, lines 27-40)

It would have been obvious to one having ordinary skills in the art at the time the invention was made to incorporate Washino's teaching into Ludtke's system so as to provide capabilities of high performance personal computer or workstation. (col.4, lines 1-6)

The modified of Ludtke discloses all the limitations above except wherein said information pertaining to said first and second functional blocks stored within said memory includes information indicative of specific capabilities of said first and second functional blocks and virtual plug information of said first and second functional blocks and the virtual plug information of said second functional block contains information indicating that the input plug of said second functional block is connected to said first functional block. However, Lym discloses the isochronous API layer manages the virtual of both input output plugs for the devices. The device is registered with the virtual plugs associates with the event. The API layer provides functions to the isochronous connection and the type of connection for transmission of isochronous data between devices on the bus. (paragraphs [197-200])

It would have been obvious to one having ordinary skills in the art at the time the invention was made to incorporate Lym's teaching into Ludtke's system so as to implement isochronous transfer features of the IEEE1394 bus very efficiently, permitting a high degree of hardware automation. [0023]

As per claims 3, 21, 28; Ludtke discloses wherein the information stored in said memory indicates that said functional block terminates data received by the data processing subunit.[0018]

As per claim 9, Ludtke discloses wherein said memory has a hierarchical · structure.(fig.3, [0039-0043]

As per claim 10, Ludtke discloses wherein said data is video data and said second functional block is a video display means that terminates said video data by converting the processed data into a video signal and displaying video corresponding thereto. [0007-0009]

As per claim 11, Ludtke discloses wherein said video display means is a display. [0040]

As per claim 13, Ludtke discloses wherein said data is audio data and said second functional block is an audio output means that terminates said audio data by converting it into sound corresponding thereto. [0007]

As per claim 17, Ludtke discloses wherein said serial data bus performs data communication in accordance with the IEEE-1394 standard. [0040]

As per claim 18, Ludtke discloses wherein said electronic apparatus is a digital television receiver. [0040]

As per claims 26, 32, Ludtke discloses a system having a plurality of electronic apparatuses connected via a serial data bus to enable transmission of data among said apparatuses, comprising:

- A data transmitting apparatus for transmitting audio/video data over said serial data bus; [0039], [0018]

- A data receiving apparatus for receiving the audio/video data transmitted by said serial data transmitting apparatus over said data bus; [0018] Wherein said data receiving apparatus comprises:

- A data processing subunit, included within said receiving apparatus, for processing said received audio/video data; [0039]

- A first functional block (fig.3, 63, ie controller), included within said data processing subunit, operative as an audio/visual processing functional block to process the audio/visual input data; [0039-0043]

- A second functional block (fig.3, 62, ie display), included within said data processing subunit, operative as terminating functional block to terminate the data processed by said first functional block by transforming the audio/visual data into a user-visible signal and outputting said user-visible signal being not processed afterward; [0039-0043], ie, the subunit 60 loads packet within the buffer 78 into the VRM circuit 64 (first functional block) to be shown on the display 62 (second functional block)

- A memory (fig.3, 78 ie buffer), [0043] for storing information pertaining to said data processing subunit and said second functional block, wherein the information stored in said memory is accessible by an external electronic apparatus connected to said electronic apparatus via said serial data bus. [0039], [0014], [0018]

  Ludtke discloses all the limitations as above except transforming the data to an image signal. However, Washino discloses interface unit

operative to convert the video program in the input format into an output

signal representative of formatted image, and output the signal to an

attached display device. (col.4, lines 27-40)

It would have been obvious to one having ordinary skills in the art

at the time the invention was made to incorporate Washino's teaching into

Ludtke's system so as to provide capabilities of high performance

personal computer or workstation. (col.4, lines 1-6)

The modified of Ludtke discloses all the limitations above except

wherein said information pertaining to said first and second functional

blocks stored within said memory includes information indicative of

specific capabilities of said first and second functional blocks and virtual

plug information of said first and second functional blocks and the virtual

plug information of said second functional block contains information

indicating that the input plug of said second functional block is connected

to said first functional block. However, Lym discloses the isochronous API

layer manages the virtual of both input output plugs for the devices. The

device is registered with the virtual plugs associates with the event. The

API layer provides functions to the isochronous connection and the type of

connection for transmission of isochronous data between devices on the

bus. (paragraphs [197-200])

It would have been obvious to one having ordinary skills in the art

at the time the invention was made to incorporate Lym's teaching into

Ludtke's system so as to implement isochronous transfer features of the IEEE1394 bus very efficiently, permitting a high degree of hardware automation. [0023]

3. Claim 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Ludtke et al. (Pub. No US 20030210252) in view of Washino (US Patent 6,370,198) and further in view of Lym et al. (Pub No US20050097245) and further in view of Henrikson (US Patent 5,923,673)

The modified of Ludtke discloses all the limitations as above except wherein said video display means is a printer. However, Henrikson discloses the captured data displayed for the user through user interface and printed on a printer for testing and monitoring the communication over the IEEE1394 serial bus. (col.2, lines 34-38)

It would have been obvious to one having ordinary skills in the art at the time the invention was made to incorporate Henrikson's teaching into Ludtke's system so as it can be easily to monitoring communications over IEEE1394. (col.2, lines 1-11)

Response to Amendment

4. Applicant's amendment filed on 7/1/05 have been fully considered but are moot in view of the new ground(s) of rejection.

Conclusion

5.   Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

6.   *Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kim Huynh whose telephone number is (571)272-3635 or via e-mail addressed to [kim.huynh3@uspto.gov]. The examiner can normally be reached on M-F 9.00AM- 6:00PM. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached at (571)272-3676 or via e-mail addressed to [rehana.perveen@uspto.gov].*

The fax phone numbers for the organization where this application or proceeding is assigned are (571)273-8300 for regular communications and After Final communications. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571)272-2100.

Kim Huynh

September 16, 2005

Khanh Dang
Primary Examiner